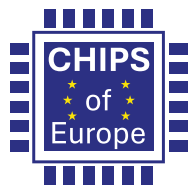




**BRNO  
UNIVERSITY  
OF TECHNOLOGY**



Creating Higher-Education  
Industry Programmes for the  
Semiconductor Industry of  
Europe



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# Summer school **Chip Design & Semiconductor Technologies**

 Brno, Czech Republic  Virtual (2.–4. 9. 2026)  
On-site (7.–11. 9. 2026)  3 ECTS

**Design, simulate and understand integrated circuits from theory to fabrication.**  
This summer school combines analog CMOS design with hands-on laboratory experience in cleanrooms and electron microscopy.

## What you will work on

- CMOS device fundamentals and modeling
- Analog IC building blocks
- Cadence & Synopsys simulation environment
- Cleanroom wafer processing
- Electron microscopy for IC analysis



## Who can apply

- MSc students (first year)
- Final-year BSc students

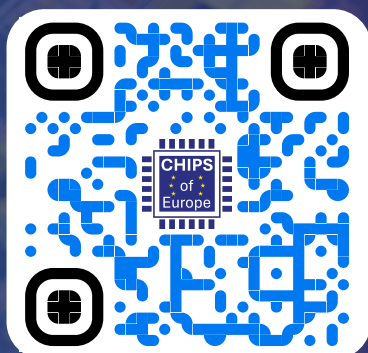
## Format

- Virtual theoretical module
- On-site project-based design
- International teams

## Limited availability

Only 20 international participants  
will be accepted

**Apply now!**



More information at  
[www.semiconductorsummerschool.eu](http://www.semiconductorsummerschool.eu)